



	Subclass	ISSUE CLASSIFICATION
	Class	

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

- O.I.P.E.

PATENT DATE

SCANNED

14

APPLICATION NO. 09/602938	CONT/PRIOR	CLASS <u>326</u> <u>311</u>	SUBCLASS <u>725</u>	ART UNIT <u>2819</u> <u>2138</u>	EXAMINER <u>MOORE</u> <u>Hee</u> <u>Detady</u>
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REFERENCES

Michael Moore

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## Method and apparatus for programmable logic device (PLD) built-in-self-test (BIST)

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

**Continued on Issue Slip Inside File Jacket**

<b>TERMINAL DISCLAIMER</b>		<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
		<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____		(Assistant Examiner) _____ (Date) _____			<b>ISSUE FEE</b>	
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